GATE	ΕΕ		www.nodia.co.in www.nodia.co.in www.nodia.co.in www.nodia.co.in www.nodia.co.in www.nodia.co.in			
2004	ww.nodia.co.in	www.nodia.co.in	www.nodia.co.in www.nodia.co.in www.nodia.co.in www.nodia.co.in	www.nodia.co.in www.nodia.co.in	www.nodia.co.in www.nodia.co.in	www.nodia.co.in www.nodia.co.in
2004						
www.nodia.co.in wy						

Q.1 - 30 Carry One Mark Each

MCQ 1.1 Consider the network graph shown in the figure. Which one of the following is NOT a 'tree' of this graph ?



SOL 1.1 For a tree there must not be any loop. So a, c, and d don't have any loop. Only b has loop.

Hence (B) is correct option.

MCQ 1.2 The equivalent inductance measured between the terminals 1 and 2 for the circuit shown in the figure is



SOL 1.2 The sign of M is as per sign of L If current enters or exit the dotted terminals of both coil. The sign of M is opposite of L If current enters in dotted terminal of a coil and exit from the dotted terminal of other coil.

Thus $L_{eq} = L_1 + L_2 - 2M$ Hence (D) is correct option.

MCQ 1.3 The circuit shown in the figure, with $R = \frac{1}{3}\Omega$, $L = \frac{1}{4}H$ and C = 3F has input voltage $v(t) = \sin 2t$. The resulting current i(t) is



MCQ 1.4 For the circuit shown in the figure, the time constant RC = 1 ms. The input voltage is $v_i(t) = \sqrt{2} \sin 10^3 t$. The output voltage $v_o(t)$ is equal to



Visit us at: www.nodia.co.in

	Impedance $Z(s) = s + 2$ $I(s) = \frac{V_i(s)}{s+2} = \frac{1}{s(s+2)}$
	or $I(s) = \frac{1}{s+2} = \frac{1}{s(s+2)}$ $I(s) = \frac{1}{2} \left[\frac{1}{s} - \frac{1}{s+2} \right]$
	Taking inverse laplace transform $i(t) = \frac{1}{2}(1 - e^{-2t})u(t)$
	At $t = 0$, $i(t) = 0$ At $t = \frac{1}{2}$, $i(t) = 0.31$ At $t = \infty$, $i(t) = 0.5$ Graph (C) satisfies all these conditions.
MCQ 1.6	The impurity commonly used for realizing the base region of a silicon $n - p - n$ transistor is (A) Gallium (C) Boron (B) Indium (D) Phosphorus
SOL 1.6	Trivalent impurities are used for making p type semiconductor. Boron is trivalent. Hence option (C) is correct 2 1 C
MCQ 1.7	If for a silicon npn transistor, the base-to-emitter voltage (V_{BE}) is 0.7 V and the collector-to-base voltage (V_{CB}) is 0.2 V, then the transistor is operating in the (A) normal active mode (B) saturation mode (C) inverse active mode (D) cutoff mode
SOL 1.7	Here emitter base junction is forward biased and base collector junction is reversed biased. Thus transistor is operating in normal active region. Hence option (A) is correct.
MCQ 1.8	 Consider the following statements S1 and S2. S1 : The β of a bipolar transistor reduces if the base width is increased. S2 : The β of a bipolar transistor increases if the dopoing concentration in the base is increased. Which remarks of the following is correct ? (A) S1 is FALSE and S2 is TRUE (B) Both S1 and S2 are TRUE (C) Both S1 and S2 are FALSE (D) S1 is TRUE and S2 is FALSE
SOL 1.8	Hence option (D) is correct. We have $\beta = \frac{\alpha}{1-\alpha}$

	Thus $\alpha \uparrow \rightarrow \beta \uparrow$ $\alpha \downarrow \rightarrow \beta \downarrow$ If the base width increases, recombination of carrier in base region increases and α decreases & hence β decreases. If doping in base region increases, recombination of carrier in base increases and α decreases thereby decreasing β . Thus S_1 is true and S_2 is false.
MCQ 1.9	An ideal op-amp is an ideal(A) voltage controlled current source(B) voltage controlled voltage source(C) current controlled current source(D) current controlled voltage source
SOL 1.9	An ideal OPAMP is an ideal voltage controlled voltage source. Hence (B) is correct option.
MCQ 1.10	Voltage series feedback (also called series-shunt feedback) results in(A) increase in both input and output impedances(B) decrease in both input and output impedances(C) increase in input impedance and decrease in output impedance(D) decrease in input impedance and increase in output impedance
SOL 1.10	In voltage series feed back amplifier, input impedance increases by factor $(1 + A\beta)$ and output impedance decreases by the factor $(1 + A\beta)$. $R_{if} = R_i(1 + A\beta)$ $R_{of} = \frac{R_o}{(1 + A\beta)}$ Hence (C) is correct option.
MCQ 1.11	The circuit in the figure is a $v_i \bullet \dots \overset{R}{\underset{c}{\overset{c}{\overset{c}{\overset{c}{\overset{c}{\overset{c}{\overset{c}{c$

- (A) low-pass filter
- (C) band-pass filter

SOL 1.11This is a Low pass filter, becauseAt $\omega = \infty$ $\frac{V_0}{V_{in}} = 0$ and at $\omega = 0$ $\frac{V_0}{V_{in}} = 1$

Hence (A) is correct option.

- (B) high-pass filter
- (D) band-reject filter

MCQ 1.12 Assuming $V_{CEsat} = 0.2$ V and $\beta = 50$, the minimum base current (I_B) required to drive the transistor in the figure to saturation is



- **VCQ 1.14** The range of signed decimal numbers that can be represented by 6-bits 1's complement number is
 - (A) -31 to +31(B) -63 to +63(C) -64 to +63(D) -32 to +31

Brought to you by: <u>Nodia and Company</u> PUBLISHING FOR GATE GATE EC 2004

SOL 1.14	The range of signed decimal numbers that can be represented by $n - bits 1$'s complement number is $-(2^{n-1}-1)$ to $+(2^{n-1}-1)$. Thus for $n = 6$ we have Range $= -(2^{6-1}-1)$ to $+(2^{6-1}-1)$ = -31 to $+31Hence (A) is correct answer.$
MCQ 1.15	A digital system is required to amplify a binary-encoded audio signal. The user should be able to control the gain of the amplifier from minimum to a maximum in 100 increments. The minimum number of bits required to encode, in straight binary, is (A) 8 (B) 6 (C) 5 (D) 7
SOL 1.15	The minimum number of bit require to encode 100 increment is $2^n \ge 100$ or $n \ge 7$ Hence (D) is correct answer.
MCQ 1.16	Choose the correct one from among the alternatives A, B, C, D after matching an item from Group 1 most appropriate item in Group 2.Group 1Group 2P. Shift register1. Frequency divisionQ. Counter2. Addressing in memory chipsR. Decoder3. Serial to parallel data conversion(A) $P-3, Q-2, R-1$ (B) $P-3, Q-1, R-2$ (C) $P-2, Q-1, R-3$ (D) $P-1, Q-2, R-2$
SOL 1.16	Shift Register \rightarrow Serial to parallel data conversion Counter \rightarrow Frequency division Decoder \rightarrow Addressing in memory chips. Hence (B) is correct answer.
MCQ 1.17	The figure the internal schematic of a TTL AND-OR-OR-Invert (AOI) gate. For the inputs shown in the figure, the output Y is $A = F_{B} = F_{D}$ Inputs are Floating
	(A) 0(B) 1(C) AB (D) \overline{AB}
SOL 1.17	For the TTL family if terminal is floating, then it is at logic 1. Thus $Y = (\overline{AB+1}) = \overline{AB} \cdot 0 = 0$

Page 7

Hence (A) is correct answer.





- (A) an NOMS inverter with enhancement mode transistor as load
- (B) an NMOS inverter with depletion mode transistor as load
- (C) a CMOS inverter
- (D) a BJT inverter

SOL 1.18 Hence option (C) is correct

- **MCQ 1.19** The impulse response h[n] of a linear time-invariant system is given by h[n] = u[n+3] + u[n-2) 2n[n-7] where u[n] is the unit step sequence. The above system is
 - (A) stable but not causal **[] []** (B) stable and causal
 - (C) causal but unstable (D) unstable and not causal
- **SOL 1.19** A system is stable if $\sum_{n=-\infty}^{\infty} |h(n)| < \infty$. The plot of given h(n) is



Hence system is stable but $h(n) \neq 0$ for n < 0. Thus it is not causal. Hence (A) is correct answer.

MCQ 1.20 The distribution function $F_x(x)$ of a random variable x is shown in the figure. The probability that X = 1 is



Visit us at: www.nodia.co.in

PUBLISHING FOR GATE

 $\angle G(j\omega) H(j\omega) = -180^{\circ} + \tan^{-1}\omega$ The frequency at which phase becomes -180° , is called phase crossover frequency. $-180 = -180^{\circ} + \tan^{-1}\omega_{\phi}$ Thus $\tan^{-1}\omega_{\phi}=0$ or $\omega_{\phi} = 0$ or The gain at $\omega_{\phi} = 0$ is $|G(j\omega)H(j\omega)| = \frac{2\sqrt{1+\omega^2}}{2} = \infty$ Thus gain margin is $= \frac{1}{2} = 0$ and in dB this is $-\infty$. Hence (D) is correct option Given $G(s)H(s) = \frac{K}{s(s+1)(s+3)}$. The point of intersection of the asymptotes of the root loci with the real axis is **MCQ 1.24** (A) - 4(B) 1.33 (C) - 1.33(D) 4 Centroid is the point where all asymptotes intersects. SOL 1.24 $\sigma = \frac{\Sigma \text{Real of Open Loop Pole} - \Sigma \text{Real Part of Open Loop Pole}}{\Sigma \text{No.of Open Loop Pole} - \Sigma \text{No.of Open Loop zero}}$ $=\frac{-1-3}{3}$ **g** 1**33 t e** Hence (C) is correct option. In a PCM system, if the code word length is increased from 6 to 8 bits, the signal **MCQ 1.25** to quantization noise ratio improves by the factor (A) $\frac{8}{6}$ (B) 12(C) 16 (D) 8 When word length is 6 **SOL 1.25** $\left(\frac{S}{N}\right)_{N=6} = 2^{2\times 6} = 2^{12}$ When word length is 8 $\left(\frac{S}{N}\right)_{\!\!N=8}=2^{2\times 8}=2^{16}$ Now $\frac{\left(\frac{S}{N}\right)_{N=8}}{\left(\frac{S}{N}\right)_{N=6}} = \frac{2^{16}}{2^{12}} = 2^4 = 16$ Thus it improves by a factor of 16. Hence (C) is correct option. An AM signal is detected using an envelop detector. The carrier frequency and **MCQ 1.26** modulating signal frequency are 1 MHz and 2 kHz respectively. An appropriate value for the time constant of the envelop detector is (A) $500\mu \sec$ (B) $20\mu \sec$ Brought to you by: Nodia and Company Visit us at: www.nodia.co.in

PUBLISHING FOR GATE

Page 11

(C) $0.2\mu \sec$ (D) $1\mu \sec$ **SOL 1.26** Hence (B) is correct option. $f_c = 1 \times 10^6 \text{ Hz}$ Carrier frequency Modulating frequency $f_m = 2 \times 10^3 \text{ Hz}$ For an envelope detector $2\pi f_c > \frac{1}{R_c} > 2\pi f_m$ $\frac{1}{2\pi f_c} < RC < \frac{1}{2\pi f_m}$ $\frac{1}{2\pi f_c} < RC < \frac{1}{2\pi f_m}$ $\frac{1}{2\pi 10^6} < RC < \frac{1}{2 \times 10^3}$ $1.59 \times 10^{-7} < RC < 7.96 \times 10^{-5}$ so, 20 μ sec sec best lies in this interval. **MCQ 1.27** An AM signal and a narrow-band FM signal with identical carriers, modulating signals and modulation indices of 0.1 are added together. The resultant signal can be closely approximated by **T C** (B) SSB with carrier d (A) broadband FM (D) SSB without carrier (C) DSB-SC Hence (B) is correct option. SOL 1.27 $S_{AM}(t) = A_c [1 + 0.1 \cos \omega_m t] \cos \omega_m t$ $s_{NBFM}(t) = A_c \cos\left[\omega_c t + 0.1\sin\omega_m t\right]$ $s(t) = S_{AM}(t) + S_{NB} f_m(t)$ $= A_c [1 + 0.1 \cos \omega_m t] \cos \omega_c t + A_c \cos (\omega_c t + 0.1 \sin \omega_m t)$ $= A_c \cos \omega_c t + A_c 0.1 \cos \omega_m t \cos \omega_c t$ $+A_c \cos \omega_c t \cos (0.1 \sin \omega_m t) - A_c \sin \omega_c t \sin (0.1 \sin \omega_m t)$ \mathbf{As} $0.1 \sin \omega_m t \cong +0.1$ to -0.1so $\cos\left(0.1\sin\omega_m t\right) \approx 1$ As when θ is small $\cos \theta \approx 1$ and $\sin \theta \simeq \theta$, thus $\sin\left(0.1\sin\omega_m t\right) = 0.1\sin\cos\omega_c t\cos\omega_m t + A_c\cos\omega_c t - A_c 0.1\sin\omega_m t\sin\omega_c t$ $= \underbrace{2A_c \cos \omega_c t}_{\text{cosec}} + \underbrace{0.1A_c \cos \left(\omega_c + \omega_m\right) t}_{USB}$ Thus it is SSB with carrier. In the output of a DM speech encoder, the consecutive pulses are of opposite **MCQ 1.28**

MCQ 1.28 In the output of a DM speech encoder, the consecutive pulses are of opposite polarity during time interval $t_1 \le t \le t_2$. This indicates that during this interval (A) the input to the modulator is essentially constant

(B) the modulator is going through slope overload

Page 12	GATE EC 2004 www.gatehelp.com
	(C) the accumulator is in saturation(D) the speech signal is being sampled at the Nyquist rate
SOL 1.28	Consecutive pulses are of same polarity when modulator is in slope overload. Consecutive pulses are of opposite polarity when the input is constant. Hence (A) is correct option.
MCQ 1.29	 The phase velocity of an electromagnetic wave propagating in a hollow metallic rectangular waveguide in the TE₁₀ mode is (A) equal to its group velocity (B) less than the velocity of light in free space (C) equal to the velocity of light in free space (D) greater than the velocity of light in free space
SOL 1.29	We know that $v_p > c > v_g$. Hence (D) is correct option.
MCQ 1.30	Consider a lossless antenna with a directive gain of +6 dB. If 1 mW of power is fee to it the total power radiated by the antenna will be (A) 4 mW (B) 1 mW (C) 7 mW D a 1 c (D) 1/4 mW
SOL 1.30	Hence (A) is correct option. We have $G_D(\theta, \phi) = \frac{4\pi U(\theta, \phi)}{P_{rad}}$ help For lossless antenna $P_{rad} = P_{in}$
	Here we have $P_{rad} = P_{in} = 1 \text{ mW}$ and $10 \log G_D(\theta, \phi) = 6 \text{ dB}$ or $G_D(\theta, \phi) = 3.98$ Thus the total power radiated by antenna is $4\pi U(\theta, \phi) = P_{rad} G_D(\theta, \phi) = 1 \text{ m} \times 3.98 = 3.98 \text{ mW}$
	Q.31 - 90 Carry Two Marks Each
MCQ 1.31	For the lattice shown in the figure, $Z_a = j2 \Omega$ and $Z_b = 2 \Omega$. The values of the open circuit impedance parameters $[z] = \begin{bmatrix} z_{11} & z_{12} \\ z_{21} & z_{22} \end{bmatrix}$ are



Brought to you by: Nodia and Company PUBLISHING FOR GATE (A) $\begin{bmatrix} 1-j & 1+j \\ 1+j & 1+j \end{bmatrix}$ (C) $\begin{bmatrix} 1+j & 1+j \\ 1-j & 1-j \end{bmatrix}$ We know that $z_{12}I_2$ $z_{22}I_2$ where

(B)
$$\begin{bmatrix} 1-j & 1+j \\ -1+j & 1-j \end{bmatrix}$$

(D)
$$\begin{bmatrix} 1+j & -1+j \\ -1+j & 1+j \end{bmatrix}$$

SOL 1.31

$$V_{1} = z_{11}I_{1} + z_{11}$$

$$V_{2} = z_{11}I_{1} + z_{11}$$

$$z_{11} = \frac{V_{1}}{I_{1}}\Big|_{I_{2}=0}$$

$$z_{21} = \frac{V_{2}}{I_{1}}\Big|_{I_{1}=0}$$

Consider the given lattice network, when $I_2 = 0$. There is two similar path in the circuit for the current I_1 . So $I = \frac{1}{2}I_1$



Thus

 $V_2 = \frac{1}{2}I_1(Z_a - Z_b)$ $z_{21} = \frac{1}{2}(Z_a - Z_b)$

For this circuit $z_{11} = z_{22}$ and $z_{12} = z_{21}$. Thus

$\begin{bmatrix} z_{11} & z_{12} \\ z_{21} & z_{22} \end{bmatrix} =$	$\frac{\frac{Z_a + Z_b}{2}}{\frac{Z_a - Z_b}{2}}$	2
e $Z_a = 2j$ and	$Z_b = 2\Omega$] }
		1]

Here Thus $\begin{vmatrix} z_{11} & z_{12} \\ z_{21} & z_{22} \end{vmatrix} = \begin{vmatrix} 1+j & j-1 \\ j-1 & 1+j \end{vmatrix}$

Hence (D) is correct option.

Brought to you by: Nodia and Company PUBLISHING FOR GATE

Visit us at: www.nodia.co.in

GATE EC 2004

MCQ 1.32 The circuit shown in the figure has initial current $i_L(0^-) = 1$ A through the inductor and an initial voltage $v_C(0^-) = -1$ V across the capacitor. For input v(t) = u(t), the Laplace transform of the current i(t) for $t \ge 0$ is



Hence (B) is correct option.





Brought to you by: Nodia and Company PUBLISHING FOR GATE

Visit us at: www.nodia.co.in

-20 dB/sec





-20



SOL 1.34 Characteristics equation is $s^2 + 20s + 10^6 = 0$ Comparing with $s^2 + 2\xi\omega_n s + \omega_n^2 = 0$ we have $\omega_n = \sqrt{10^6} = 10^3$ $2\xi\omega = 20$ Thus $2\xi = \frac{20}{10^3} = 0.02$ Now $Q = \frac{1}{2\xi} = \frac{1}{0.02} = 50$ Hence (B) is correct option.

MCQ 1.35 For the circuit shown in the figure, the initial conditions are zero. Its transfer function $H(s) = \frac{V_c(s)}{V_i(s)}$ is



SOL 1.35 Hence (D) is correct option.

$$H(s) = \frac{V_0(s)}{V_i(s)}$$

$$= \frac{\frac{1}{sC}}{R + sL + \frac{1}{sC}} = \frac{1}{s^2 LC + sCR + 1}$$

$$= \frac{1}{s^2(10^{-2} \times 10^{-4}) + s(10^{-4} \times 10^4) + 1}$$

$$= \frac{1}{10^{-6}s^2 + s + 1} = \frac{10^6}{s^2 + 10^6 s + 10^6}$$

MCQ 1.36 A system described by the following differential equation $\frac{d^2 y}{dt^2} + 3\frac{dy}{dt} + 2y = x(t)$ is initially at rest. For input x(t) = 2u(t), the output y(t) is (A) $(1 - 2e^{-t} + e^{-2t})u(t)$ (B) $(1 + 2e^{-t} - 2e^{-2t})u(t)$ (C) $(0.5 + e^{-t} + 1.5e^{-2t})u(t)$ (D) $(0.5 + 2e^{-t} + 2e^{-2t})u(t)$

SOL 1.36 Hence Correct Option is (A) Given, $\frac{d^2y}{dt^2} + 3\frac{dy}{dt} + 2y = x(t)$

Taking Laplace Transformation both sides, we have

$$[s^{2}+3s+2]Y(s) = X(s) = \frac{2}{s}$$

or

$$Y(s) = \frac{2}{s(s+1)(s+2)} = \frac{1}{s} - \frac{2}{s+1} + \frac{1}{s+2}$$

Increasing Laplace transformation gives,

$$y(t) = (1 - 2e^{-t} + e^{-2t})u(t)$$

MCQ 1.37Consider the following statements S1 and S2
S1 : At the resonant frequency the impedance of a series RLC circuit is zero.
S2 : In a parallel GLC circuit, increasing the conductance G results in increase in
its Q factor.

Which one of the following is correct?

- (A) S1 is FALSE and S2 is TRUE
- (B) Both S1 and S2 are TRUE
- (C) S1 is TRUE and S2 is FALSE
- (D) Both S1 and S2 are FALSE
- **SOL 1.37** Impedance of series RLC circuit at resonant frequency is minimum, not zero. Actually imaginary part is zero.

$$Z = R + j \left(\omega L - \frac{1}{\omega C} \right)$$

At resonance $\omega L - \frac{1}{\omega C} = 0$ and Z = R that is purely resistive. Thus S_1 is false $Q = R \sqrt{\frac{C}{L}}$ Now quality factor

Since
$$G = \frac{1}{R}$$
, $Q = \frac{1}{G}\sqrt{\frac{G}{R}}$

If $G \uparrow$ then $Q \downarrow$ provided C and L are constant. Thus S_2 is also false. Hence (D) is correct option.

In an abrupt p-n junction, the doping concentrations on the p-side and n-side **MCQ 1.38** are $N_A = 9 \times 10^{16}$ /cm³ respectively. The p - n junction is reverse biased and the total depletion width is 3 μ m. The depletion width on the p-side is (Λ) 0 7

(A) 2.7
$$\mu$$
m
(C) 2.25 μ m
(C) 2.25 μ m
(B) 0.3 μ m
(C) 0.75 μ m

We know that **SOL 1.38** $W_{\star} N_{\star} = W N_{\star}$

or

$$W_p N_A = W_n N_D$$

 $W_p = rac{W_n imes N_D}{N_A} = rac{3 \, \mu imes 10^{16}}{9 imes 10^{16}} = 0.3 \, \mu \mathrm{m}$

Hence option (B) is correct.

MCQ 1.39 The resistivity of a uniformly doped n-type silicon sample is 0.5Ω - mc. If the electron mobility (μ_n) is 1250 cm²/V-sec and the charge of an electron is 1.6×10^{-19} Coulomb, the donor impurity concentration (N_D) in the sample is

(A)
$$2 \times 10^{16}$$
/cm³
(B) 1×10^{16} /cm³
(C) 2.5×10^{15} /cm³
(D) 5×10^{15} /cm³

SOL 1.39 Hence option (B) is correct.

or resistivity

Thus

Conductivity $\sigma = nqu_n$ $\rho = \frac{1}{\sigma} = \frac{1}{nq\mu_n}$ $n = \frac{1}{q\rho\mu_n} = \frac{1}{1.6 \times 10^{-19} \times 0.5 \times 1250} = 10^{16} / \text{cm}^3$

For *n* type semiconductor $n = N_D$

Page 18

MCQ 1.40 Consider an abrupt p - n junction. Let V_{bi} be the built-in potential of this junction and V_R be the applied reverse bias. If the junction capacitance (C_i) is 1 pF for $V_{bi} + V_R = 1$ V, then for $V_{bi} + V_R = 4$ V, C_j will be (A) 4 pF(B) 2 pF(C) 0.25 pF (D) 0.5 pF

SOL 1.40 We know that

$$C_{j} = \left[\frac{e\varepsilon_{S}N_{A}N_{D}}{2(V_{bi}+V_{R})(N_{A}+N_{D})}\right]^{\frac{1}{2}}$$

Thus $C_{j} \propto \sqrt{\frac{1}{(V_{bi}+V_{R})}}$
Now $\frac{C_{j2}}{C_{j1}} = \sqrt{\frac{(V_{bi}+V_{R})_{1}}{(V_{bi}+V_{R})_{2}}} = \sqrt{\frac{1}{4}} = \frac{1}{2}$
or $C_{j2} = \frac{C_{j1}}{2} = \frac{1}{2} = 0.5 \text{ pF}$

or

Now

Hence option (D) is correct.

- MCQ 1.41 Consider the following statements Sq and S2.
 - S1 : The threshold voltage (V_T) of MOS capacitor decreases with increase in gate oxide thickness.
 - S2: The threshold voltage (V_T) of a MOS capacitor decreases with increase in substrate doping concentration.
 - Which Marks of the following is correct?
 - (A) S1 is FALSE and S2 is TRUE
 - (B) Both S1 and S2 are TRUE
 - (C) Both S1 and S2 are FALSE
 - (D) S1 is TRUE and S2 is FALSE
- SOL 1.41 Increase in gate oxide thickness makes difficult to induce charges in channel. Thus V_T increases if we increase gate oxide thickness. Hence S_1 is false. Increase in substrate doping concentration require more gate voltage because initially induce charges will get combine in substrate. Thus V_T increases if we increase substrate doping concentration. Hence S_2 is false. Hence option (C) is correct.
- MCQ 1.42 The drain of an n-channel MOSFET is shorted to the gate so that $V_{GS} = V_{DS}$. The threshold voltage (V_T) of the MOSFET is 1 V. If the drain current (I_D) is 1 mA for $V_{GS} = 2$ V, then for $V_{GS} = 3$ V, I_D is (A) 2 mA(B) 3 mA

(D) 4 mA

(C) 9 mA

SOL 1.42 We know that $I_D = K(V_{GS} - V_T)^2$ $\frac{I_{DS}}{I_{DI}} = \frac{(V_{GS2} - V_T)^2}{(V_{GS1} - V_T)^2}$ Thus Substituting the values we have $\frac{I_{D2}}{I_{D1}} = \frac{(3-1)^2}{(2-1)^2} = 4$ $I_{D2} = 4I_{DI} = 4 \text{ mA}$ or Hence option (D) is correct. **MCQ 1.43** The longest wavelength that can be absorbed by silicon, which has the bandgap of 1.12 eV, is 1.1 μ m. If the longest wavelength that can be absorbed by another material is 0.87 μ m, then bandgap of this material is (A) 1.416 A/cm^2 (B) 0.886 eV (C) 0.854 eV (D) 0.706 eV Hence option (A) is correct. **SOL 1.43** $E_g \propto \frac{1}{\lambda}$ $\frac{E_{g2}}{E_{g1}} = \frac{\lambda_1}{\lambda_2} = \frac{1.10}{0.87}$ **d l C** Thus $E_{g2} = \frac{1.1}{0.87} \times 1.12 = 1.416$ eV2 or The neutral base width of a bipolar transistor, biased in the active region, is 0.5 μ **MCQ 1.44** m. The maximum electron concentration and the diffusion constant in the base are 10^{14} /cm³ and $D_n = 25$ cm²/sec respectively. Assuming negligible recombination in the base, the collector current density is (the electron charge is 1.6×10^{-19} Coulomb) (A) 800 A/cm^2 (B) 8 A/cm^2

(C)
$$200 \text{ A/cm}^2$$
 (D) 2 A/cm^2

SOL 1.44 Concentration gradient

$$\frac{dn}{dx} = \frac{10^{14}}{0.5 \times 10^{-4}} = 2 \times 10^{18}$$

$$q = 1.6 \times 10^{-19} C$$

$$D_n = 25$$

$$\frac{dn}{dx} = \frac{10^{14}}{0.5 \times 10^{-4}}$$

$$J_C = q D_n \frac{dn}{dx}$$

$$= 1.6 \times 10^{-19} \times 25 \times 2 \times 10^{18} = 8 \text{ A/cm}^2$$

Hence option (B) is correct.

GATE EC 2004

Assume that the β of transistor is extremely large and $V_{BE} = 0.7 V, I_C$ and V_{CE} in **MCQ 1.45** the circuit shown in the figure



(A)
$$I_C = 1 \text{ mA}, V_{CE} = 4.7 \text{ V}$$

(C) $I_C = 1 \text{ mA}, V_{CE} = 2.5 \text{ V}$

(B) $I_C = 0.5 \text{ mA}, V_{CE} = 3.75 \text{ V}$ (D) $I_C = 0.5 \text{ mA}, V_{CE} = 3.9 \text{ V}$



The theorem equivalent is shown below



Since β is large is large, $I_C \approx I_E$, $I_B \approx 0$ and $I_E = \frac{V_T - V_{BE}}{R_E} = \frac{1 - 0.7}{300} = 3 \text{ mA}$

Now
$$V_{CE} = 5 - 2.2 \text{k} I_C - 300 I_E$$

= 5 - 2.2k × 1m - 300 × 1m
= 2.5 V

Hence (C) is correct option

MCQ 1.46 A bipolar transistor is operating in the active region with a collector current of 1 mA. Assuming that the β of the transistor is 100 and the thermal voltage (V_T) is 25 mV, the transconductance (g_m) and the input resistance (r_{π}) of the transistor in the common emitter configuration, are

- (A) $g_m = 25 \text{ mA/V}$ and $r_\pi = 15.625 \text{ k}\Omega$
- (B) $g_m = 40 \text{ mA/V}$ and $r_\pi = 4.0 \text{ k}\Omega$
- (C) $g_m = 25 \text{ mA/V}$ and $r_{\pi} = 2.5 \text{ k} \Omega$
- (D) $g_m = 40 \text{ mA/V}$ and $r_\pi = 2.5 \text{ k}\Omega$

GATE EC 2004

When $|I_C| >> |I_{CO}|$ $g_m = \frac{|I_C|}{V_T} = \frac{1\text{mA}}{25\text{mV}} = 0.04 = 40 \text{ mA/V}$ **SOL 1.46** $r_{\pi} = \frac{\beta}{g_m} = \frac{100}{40 \times 10^{-3}} = 2.5 \text{ k}\Omega$

Hence (D) is correct option.

MCQ 1.47 The value of C required for sinusoidal oscillations of frequency 1 kHz in the circuit of the figure is



SOL 1.47

The given circuit is wein bridge oscillator. The frequency of oscillation is $2\pi f = \frac{1}{RC}$

or

 $C = \frac{1}{2\pi Rf} = \frac{1}{2\pi \times 10^3 \times 10^3} = \frac{1}{2\pi}\mu$

Hence (A) is correct option.

MCQ 1.48

In the op-amp circuit given in the figure, the load current i_L is



(D) $\frac{V_s}{R_1}$

(C)
$$-\frac{V_s}{R_L}$$



The circuit is as shown below



We know that for ideal OPAMP $V_{-} = V_{+}$ Applying KCL at inverting terminal $\frac{V_{-}-V_{s}}{R_{1}} + \frac{V_{-}-V_{0}}{R_{1}} = 0$ $2V_{-}-V_{o}=V_{s}$...(1)or Applying KCL at non-inverting terminal $\frac{V_{+}}{R_{2}} + I_{L} + \frac{V_{+} - V_{0}}{R_{2}} = 0$ or $2V_+ - V_o + I_L R_2 = 0$ Since $V_- = V_+$, from (1) and (2) we have ...(2) $V_s + I_L R_2 = 0$ $I_L = -\frac{V_s}{R_2}$ or

Hence (A) is correct option.

MCQ 1.49 In the voltage regulator shown in the figure, the load current can vary from 100 mA to 500 mA. Assuming that the Zener diode is ideal (i.e., the Zener knee current is negligibly small and Zener resistance is zero in the breakdown region), the value of R is





If I_Z is negligible the load current is

	$\frac{12-V_z}{R} = I_L$	
	as per given condition $100 \text{ mA} \leq \frac{12 - V_Z}{R} \leq 500 \text{ mA}$	
	At $I_L = 100 \text{ mA} \frac{12-5}{R} = 100 \text{ mA}$	$V_Z = 5 \mathrm{V}$
	or $R = 70\Omega$ At $I_L = 500$ mA $\frac{12-5}{R} = 500$ mA	$V_Z = 5 \mathrm{V}$
	or $R = 14 \Omega$ Thus taking minimum we get $R = 14 \Omega$	
	Hence (D) is correct option.	
MCQ 1.50	of the voltage respectively across a resist of the diode, then the appropriate relati (A) $V_{dc} = \frac{V_m}{\pi}$, $PIV = 2V_m$	(B) $I_{dc} = 2 \frac{V_m}{\pi}, PIV = 2 V_m$
	(C) $V_{dc} = 2\frac{V_m}{\pi}$, $PIV = V_m$ g ate	(D) $V_{dc} \frac{V_m}{\pi}$, $PIV = V_m$
SOL 1.50	Hence (B) is correct option.	el n
MCQ 1.51	The minimum number of 2- to -1 mu multiplexers is	ultiplexers required to realize a 4- to -1
	(A) 1	(B) 2
	(C) 3	(D) 4
SOL 1.51	Number of MUX is $\frac{4}{3} = 2$ and $\frac{2}{2} = 1$. required. Hence (C) is correct answer.	Thus the total number 3 multiplexers is
MCQ 1.52	The Boolean expression $AC + B\overline{C}$ is eq	uivalent to
	(A) $\overline{A}C + B\overline{C} + AC$	(B) $\overline{B}C + AC + B\overline{C} + \overline{A}C\overline{B}$
	(C) $AC + B\overline{C} + \overline{B}C + ABC$	(D) $ABC + \overline{A}B\overline{C} + AB\overline{C} + A\overline{B}C$
SOL 1.52	Hence (D) is correct answer. $AC + B\overline{C} = AC1 + B\overline{C}1$ $= AC(B + \overline{B}) + B\overline{C}(A + B\overline{C})$ $= ACB + AC\overline{B} + B\overline{C}A + B\overline{C}A$	
MCQ 1.53		's complement representation of which one
	of the following sets of number (A) 25,9, and 57 respectively	(B) -6, -6, and -6 respectively

Brought to you by: Nodia and Company PUBLISHING FOR GATE

Visit us at: www.nodia.co.in

(C) -7, -7 and -7 respectively(D) -25, -9 and -57 respectivelySol 1.53Hence (C) is correct answer. 11001 1001111001 00110 0110000110 $+1$ $+1$ $+1$ 00111 000111 7 7 Thus 2's complement of 11001, 1001 and 111001 is 7. So the number given question are 2's complement correspond to -7.MCQ 1.54The 8255 Programmable Peripheral Interface is used as described below.(i) An A/D converter is interface to a microprocessor through an 8255. The conversion is initiated by a signal from the 8255 on Port C. A signal or C causes data to be stobed into Port A.(ii) Two computers exchange data using a pair of 8255s. Port A works bidirectional data port supported by appropriate handshaking signals. The appropriate modes of operation of the 8255 for (i) and (ii) would be (A) Mode 0 for (i) and Mode 1 for (ii)(B) Mode 1 for (i) and Mode 2 for (ii)(D) Mode 2 for (i) and Mode 1 for (iii)(D) Mode 2 for (i) and Mode 1 for (iii)(D) Mode 2 for (i) and Mode 1 for (iii)(D) Mode 2 for (i) and Mode 1 for (iii)(D) Mode 1 : Input or output with hand shake In this mode following actions are executed 1. Two port (A & B) function as 8 - bit input output ports.														GAT	TE E	C 2	004								W	vwv	w.gat	ehe	lp.c	or
$11001 1001 111001 000110 \\ \frac{+1}{00111} \frac{+1}{0111} \frac{+1}{000111} \\ \frac{-1}{000111} \frac{-1}{000111} \\ \frac{-1}{7} \frac{-1}{7} \frac{-1}{7} \\ Thus 2's complement of 11001, 1001 and 111001 is 7. So the number given question are 2's complement correspond to -7. \\ MCQ 1.54 The 8255 Programmable Peripheral Interface is used as described below. (i) An A/D converter is interface to a microprocessor through an 8255. The conversion is initiated by a signal from the 8255 on Port C. A signal on C causes data to be stobed into Port A. (ii) Two computers exchange data using a pair of 8255s. Port A works bidirectional data port supported by appropriate handshaking signals. The appropriate modes of operation of the 8255 for (i) and (ii) would be (A) Mode 0 for (i) and Mode 1 for (ii) (B) Mode 1 for (i) and Mode 2 for (ii) (C) Mode for (i) and Mode 1 for (ii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 1 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) for (D) fo$	-7,	C) -	(C)	(C)	!) -7	7, -7	ar	nd	1 -7	7 re	resp	pect	ive	ly				(D)	-25	5, -9	and	l -5	7 re	espective states of the second states of the seco	ect	tiv€	əly			
 00110 0110 000010 +1 +1 +1 +1 +1 000111 000111 7 7 7 7 Thus 2's complement of 11001, 1001 and 111001 is 7. So the number given question are 2's complement correspond to -7. MCQ 1.54 The 8255 Programmable Peripheral Interface is used as described below. (i) An A/D converter is interface to a microprocessor through an 8255. The conversion is initiated by a signal from the 8255 on Port C. A signal on C causes data to be stobed into Port A. (ii) Two computers exchange data using a pair of 8255s. Port A works bidirectional data port supported by appropriate handshaking signals. The appropriate modes of operation of the 8255 for (i) and (ii) would be (A) Mode 0 for (i) and Mode 1 for (ii) (B) Mode 1 for (i) and Mode 2 for (ii) (C) Mode for (i) and Mode 0 for (ii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 1 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 3 for (iii) (D) Mode 2 for (i) and Mode 4 for (iii) (D) Mode 2 for (i) and Mode 4 for (iii) (D) Mode 2 for (i) and Mode 5 for (iii) (D) Mode 2 for (i) and Mode 4 for (iii) (D) Mode 2 for (i) and Mode 4 for (iii) (D) Mode 2 for (i) and Mode 4 for (iii) (D) Mode 2 for (i) and Mode 4 for (iii) (D) Mode 2 for (i) and Mode 4 for (iii) (D) Mode 2 for (i) and Mode 4 for (iii) (D) Mode 2 for (i) and Mode 4 for (iii) (D) Mode 2 for (i) and Mode 5 for (iii) (D) Mode 2 for (i) and Mode 4 for (iii) (D) Mode 5 for (i) and Mode 4 for (iii) (D) Mode 4 for (i) and Mode 4 for (iii) (D) Mode 5 for (i) and Mode 4 for (iii) (D) Mode 4 for (i) and Mode 4 for (iii) (D) Mode 5 for (i) and Mode 4 for (iii) (D) Mode 4 for (i) and Mode 4 for (iii) (D) Mode 5 for (i) and Mode 4 for (iii) (D) Mode 5 for (i) and Mode 4 for (iii) (D) Mode 5 for (i) and Mode 5 for (ii) (D) for (i)	ce	Ienc	Hei	Her	enc€	e (C) is	s c	cor	rre	ect	ans	we	r.																
 +1/00111 0111 000111 7 7 7 7 7 Thus 2's complement of 11001, 1001 and 111001 is 7. So the number given question are 2's complement correspond to -7. MCQ 1.54 The 8255 Programmable Peripheral Interface is used as described below. (i) An A/D converter is interface to a microprocessor through an 8255. The conversion is initiated by a signal from the 8255 on Port C. A signal or C causes data to be stobed into Port A. (ii) Two computers exchange data using a pair of 8255s. Port A works bidirectional data port supported by appropriate handshaking signals. The appropriate modes of operation of the 8255 for (i) and (ii) would be (A) Mode 0 for (i) and Mode 1 for (ii) (B) Mode 1 for (i) and Mode 2 for (ii) (C) Mode for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (E) Mode 1 : Input or output with hand shake In this mode following actions are executed 								-	11	.00)1			100	L			111	001											
001110111000111777Thus 2's complement of 11001, 1001 and 111001 is 7. So the number given question are 2's complement correspond to -7.MCQ 1.54The 8255 Programmable Peripheral Interface is used as described below. (i) An A/D converter is interface to a microprocessor through an 8255. The conversion is initiated by a signal from the 8255 on Port C. A signal on C causes data to be stobed into Port A. (ii) Two computers exchange data using a pair of 8255s. Port A works bidirectional data port supported by appropriate handshaking signals. The appropriate modes of operation of the 8255 for (i) and (ii) would be (A) Mode 0 for (i) and Mode 1 for (ii) (B) Mode 1 for (i) and Mode 2 for (ii) (C) Mode for (i) and Mode 0 for (ii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 1 for (i) and Mode 1 for (iii) (D) for (i) and Mode 1 for (iii) (D) for (i) and Mode 1 for (iii) (D) for (i) and Mode 1 for (ii) (D) for (i) and for (ii) (D) for								(00)11(10			0110)			000	110	I										
 7 7 7 7 Thus 2's complement of 11001, 1001 and 111001 is 7. So the number given question are 2's complement correspond to -7. MCQ 1.54 The 8255 Programmable Peripheral Interface is used as described below. (i) An A/D converter is interface to a microprocessor through an 8255. The conversion is initiated by a signal from the 8255 on Port C. A signal on C causes data to be stobed into Port A. (ii) Two computers exchange data using a pair of 8255s. Port A works bidirectional data port supported by appropriate handshaking signals. The appropriate modes of operation of the 8255 for (i) and (ii) would be (A) Mode 0 for (i) and Mode 1 for (ii) (B) Mode 1 for (i) and Mode 2 for (ii) (C) Mode for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 1 for (i) and Mode 1 for (iii) (D) Mode 1 for (i) and Mode 1 for (iii) (D) Mode 1 for (i) and Mode 1 for (iii) (D) Mode 1 for (i) and Mode 1 for (iii) (D) Mode 1 for (i) and Mode 1 for (iii) (D) Mode 1 for (i) and Mode 1 for (iii) (D) Mode 1 for (i) and Mode 1 for (iii) (D) Mode 1 for (i) and Mode 1 for (iii) (D) Mode 1 for (i) and Mode 1 for (iii) (D) Mode 1 for (i) and Mode 1 for (iii) (D) Mode 1 for (i) and Mode 1 for (iii) (D) Mode 1 for (i) and Mode 1 for (iii) (D) Mode 1 for (i) and Mode 1 for (iii) 										+	-1																			
 Thus 2's complement of 11001, 1001 and 111001 is 7. So the number given question are 2's complement correspond to -7. MCQ 1.54 The 8255 Programmable Peripheral Interface is used as described below. (i) An A/D converter is interface to a microprocessor through an 8255. The conversion is initiated by a signal from the 8255 on Port C. A signal or C causes data to be stobed into Port A. (ii) Two computers exchange data using a pair of 8255s. Port A works bidirectional data port supported by appropriate handshaking signals. The appropriate modes of operation of the 8255 for (i) and (ii) would be (A) Mode 0 for (i) and Mode 1 for (ii) (B) Mode 1 for (i) and Mode 2 for (ii) (C) Mode for (i) and Mode 1 for (ii) (D) Mode 2 for (i) and Mode 1 for (ii) (D) Mode 2 for (i) and Mode 1 for (ii) (D) Mode 2 for (i) and Mode 1 for (ii) (D) Mode 1 for (i) and Mode 1 for (ii) (D) Mode 1 for (i) and Mode 1 for (ii) (D) Mode 2 for (i) and Mode 1 for (ii) (D) Mode 1 for (i) and Mode 3 following. Mode 1 : Input or output with hand shake In this mode following actions are executed 								(00)11	11			011	1			000	111											
 question are 2's complement correspond to -7. MCQ 1.54 The 8255 Programmable Peripheral Interface is used as described below. (i) An A/D converter is interface to a microprocessor through an 8255. The conversion is initiated by a signal from the 8255 on Port C. A signal on C causes data to be stobed into Port A. (ii) Two computers exchange data using a pair of 8255s. Port A works bidirectional data port supported by appropriate handshaking signals. The appropriate modes of operation of the 8255 for (i) and (ii) would be (A) Mode 0 for (i) and Mode 1 for (ii) (B) Mode 1 for (i) and Mode 2 for (ii) (C) Mode for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 1 for (iii) (D) Mode 2 for (i) and Mode 3 for (iii) (D) Mode 2 for (i) and Mode 3 for (iii) (D) Mode 2 for (i) and Mode 3 for (iii) (D) Mode 4 for (i) and Mode 3 for (iii) (D) Mode 4 for (i) and Mode 4 for (iii) (D) Mode 5 for (i) and Mode 4 for (iii) (D) Mode 5 for (i) and Mode 4 for (iii) (D) Mode 5 for (i) and Mode 4 for (iii) (D) Mode 5 for (i) and Mode 4 for (iii) (D) Mode 5 for (i) and Mode 4 for (iii) (D) Mode 5 for (i) and Mode 4 for (iii) (D) Mode 5 for (i) and Mode 4 for (iii) (D) Mode 5 for (i) and Mode 4 for (iii) (D) Mode 5 for (i) and Mode 4 for (iii) (D) Mode 5 for (i) and Mode 4 for (iii) (D) Mode 6 for (i) and Mode 5 for (iii) (D) Mode 6 for (i) and Mode 4 for (iii) (D) Mode 6 for (i) and mode 5 for (iii) (D) Mode 6 for (i) and mode 6 for (iii) (D) Mode 6 for (ii) (D) Mode 6 for (ii) (D) Mode 6 for (ii) (D) Mode 7 for (ii)<										,	7				7				7	7										
 MCQ 1.54 The 8255 Programmable Peripheral Interface is used as described below. (i) An A/D converter is interface to a microprocessor through an 8255. The conversion is initiated by a signal from the 8255 on Port C. A signal on C causes data to be stobed into Port A. (ii) Two computers exchange data using a pair of 8255s. Port A works bidirectional data port supported by appropriate handshaking signals. The appropriate modes of operation of the 8255 for (i) and (ii) would be (A) Mode 0 for (i) and Mode 1 for (ii) (B) Mode 1 for (i) and Mode 2 for (ii) (C) Mode for (i) and Mode 0 for (ii) (D) Mode 2 for (i) and Mode 1 for (ii) (E) Mode 1 : Input or output with hand shake In this mode following actions are executed 	Thus 2's complement of 11001, 1001 and 111001 is 7. So the number given in the																													
 (i) An A/D converter is interface to a microprocessor through an 8255. The conversion is initiated by a signal from the 8255 on Port C. A signal or C causes data to be stobed into Port A. (ii) Two computers exchange data using a pair of 8255s. Port A works bidirectional data port supported by appropriate handshaking signals. The appropriate modes of operation of the 8255 for (i) and (ii) would be (A) Mode 0 for (i) and Mode 1 for (ii) (B) Mode 1 for (i) and Mode 2 for (ii) (C) Mode for (i) and Mode 0 for (ii) (D) Mode 2 for (i) and Mode 1 for (iii) (E) For 8255, various modes are described as following. Mode 1 : Input or output with hand shake In this mode following actions are executed 	question are 2's complement correspond to -7.																													
SOL 1.54For 8255, various modes are described as following.Mode 1 : Input or output with hand shakeIn this mode following actions are executed	An co auso Tw rect ap Mo Mo	i) A: The o C cau ii) T oidire The a A) M B) M C) M	(i) The C c (ii) bid The (A) (B) (C)	(i) free The C c c (ii) bid: The (A) (B) (C)	Anne c cau) T dire ne a () M () M	n A/ conve uses Ewo ectio: appre Mode Mode	'D ers dat co nal opr e 0 e 1 e fc	cc sion ta om ta om ta ria fo fo fo	con on i a to npu dat ate or or (i)	is i o b ute ta j e m (i) (i)	erter init be s ers por nod) an) an	er is tiat stob exc ort s les nd 1 nd 1 nd 1	int ed bed cha up of of Mo	terfa by a intc nge port- opera de 1 de 2 e 0 fc	ce to a sig o Po dat ed b ation for for for	o a gnal ort 1 oy a n o (ii) (ii)	mi fre A. sir pp f th C	acroj om ng a ropi ne 8	pro the papa riat 255	cess 825 air e ha	or t 55 of of 8 ands	hrou n Po 3255 hak	ugh ort os. ing	an C. Poi sig	n 8 A rt gna	255 sig A als.	5. gnal wo			
	823 le 1 his Tw	For 8 Aode n th	For Mo In t 1.	For Mo In t 1.	or 82 ode thi T	255, e 1 : is mo fwo j	va Inj ode po:	arie pu e f ort	iou ut (fol: ; (<i>A</i>	us n or llov A &	mo ou win & E	odes itpu ig a B) f	ar It v ctio	e des vith ons a ction	scrib han are e 1 as	bed .d s exe 8 -	as hal cut bit	foll ce ed t inp	owi	out	-	-								
2. Each port uses three lines from C as a hand shake signal							-										as	a h	and	l sha	ake	sign	al							
	3. Input & output data are latched.																													
Form (ii) the mode is 1.	Form (ii) the mode is 1.																													
Mode 2 : Bi-directional data transfer This mode is used to transfer data between two computer. In this mode port																														

This mode is used to transfer data between two computer. In this mode port A can be configured as bidirectional port. Port A uses five signal from port C as hand shake signal.

For (1), mode is 2

Hence (D) is correct answer.

MCQ 1.55 The number of memory cycles required to execute the following 8085 instructions
 (i) LDA 3000 H
 (ii) LXI D, FOF1H
 would be

Page 25	GATE EC 2004	www.gatehelp.com
		for (i) and 3 for (ii) for (i) and 4 for (ii)
SOL 1.55	 LDA 16 bit ⇒ Load accumulator directly this memory location (specified within the instruction It takes 4 memory cycle-as following. 1. in instruction fetch 2. in reading 16 bit address 	÷ •
	 in copying data from memory to accumula LXI D, (F0F1)₄ ⇒ It copies 16 bit data into re It takes 3 memory cycles. Hence (B) is correct answer. 	
MCQ 1.56	In the modulo-6 ripple counter shown in figure used to clear the J-K flip-flop The 2-input gate is 1 C J B J A J	e, the output of the 2- input gate is



(\mathbf{A})	a NAND gate
(C)	an OR gate

(B) a NOR gate(D) a AND gare

SOL 1.56 In the modulo - 6 ripple counter at the end of sixth pulse (i.e. after 101 or at 110) all states must be cleared. Thus when CB is 11 the all states must be cleared. The input to 2-input gate is \overline{C} and \overline{B} and the desired output should be low since the CLEAR is active low Thus when \overline{C} and \overline{B} are 0, 0, then output must be 0. In all other case the output

must be 1. OR gate can implement this functions. Hence (C) is correct answer.

MCQ 1.57 Consider the sequence of 8085 instructions given below

LXI H, 9258 MOV A, M CMA MOV M, A Which one of the following is performed by this sequence ? (A) Contents of location 9258 are moved to the accumulator

Page 26	GATE EC 2004	www.gatehelp.con
	(B) Contents of location 9258 are compared with the cont(C) Contents of location 8529 are complemented and store(D) Contents of location 5892 are complemented and store	ed in location 8529
SOL 1.57	Hence (A) is correct answer. LXI H, 9258H ; 9258H \rightarrow HL MOV A, M ; (9258H) \rightarrow A CMa ; $\overline{A} \rightarrow A$ MOV M, A ; $A \rightarrow M$ This program complement the data of memory location 92	258H.
MCQ 1.58	A Boolean function f of two variables x and y is defined $f(0,0) = f(0,1) = f(1,1) = 1; f(1,0) = 0$ Assuming complements of x and y are not available, a mirealizing f using only 2-input NOR gates and 2- input OR cost) would have a total cost of (A) 1 unit (C) 3 unit (D) 2 unit	inimum cost solution for
SOL 1.58	Hence (D) is correct answer. We have $f(x,y) = \overline{xy} + \overline{xy} + \overline{xy} + xy = \overline{x}(\overline{y} + y) + xy = \overline{x} - \overline{y}$ or $f(x,y) = \overline{x} + \overline{y}$ Here compliments are not available, so to get \overline{x} we use I circuit require 1 unit OR and 1 unit NOR gate giving tota	NOR gate. Thus desired
MCQ 1.59	It is desired to multiply the numbers 0AH by 0BH and accumulator. The numbers are available in registers B and of the 8085 program for this purpose is given below : MVI A, 00H LOOP HLT END The sequence of instructions to complete the program wou (A) JNX LOOP, ADD B, DCR C (B) ADD B, JNZ LOOP, DCR C (C) DCR C, JNZ LOOP, ADD B (D) ADD B, DCR C, JNZ LOOP	d C respectively. A part
SOL 1.59	Hence (D) is correct answer. MVI A, 00H ; Clear accumulator	

PUBLISHING FOR GATE

	DCR C ; Decrement C JNZ LOOP ; If C is not zero jump to loop HLT END
	This instruction set add the contents of B to accumulator to contents of C times. Hence (D) is correct answer.
MCQ 1.60	A 1 kHz sinusoidal signal is ideally sampled at 1500 samples/sec and the sampled signal is passed through an ideal low-pass filter with cut-off frequency 800 Hz. The output signal has the frequency. (A) zero Hz (B) 0.75 kHz (C) 0.5 kHz (D) 0.25 kHz
SOL 1.60	Hence Correct Option is (C) Here $f_s = 1500$ samples/sec, $f_m = \text{kHz}$ The sampled frequency are 2.5 kHz, 0.5 kHz, Since LPF has cut-off frequency 800 Hz, then only output signal of frequency 0.5 kHz would pass through it
MCQ 1.61	A rectangular pulse train $s(t)$ as shown in the figure is convolved with the signal $\cos^2(4p \times 10^3 t)$. The convolved signal will be a $\underbrace{x(t)}_{0.1 \text{ msec}} \underbrace{0.1 \text{ msec}}_{t} \underbrace{0.1 \text{ msec}}_{t}$ (A) DC (B) 12 kHz sinusoid
SOL 1.61	(C) 8 kHz sinusoid Hence Correct Option is (D) $S(t) = \frac{1}{T_s} [1 + 2\cos \omega_s t + 2\cos 2\omega_s t + \dots]$ $\cos^2 4\pi \times 10^3 t = \frac{(1 + \cos 8\pi \times 10^3 t)}{2}$ $\omega_s = \frac{2\pi}{0.1 \times 10^{-3}} = 2\pi \times 10 \times 10^3$ $S(t)^* x(t) = \int_{-\infty}^{\infty} S(\tau) \times (\tau - t) d\tau$ $= \int_{-\infty}^{\infty} 10 \times 10^3 [1 + 2\cos \omega_s t + 2\cos 2\omega_s t + \dots] dt$ $[1 + \cos 8\pi \times 10^3 t]$
	$\times \frac{\left[1 + \cos 8\pi \times 10^3 t\right]}{2}$

Visit us at: www.nodia.co.in

So, frequencies present will be $f_s \pm f_m, 2f_s \pm 3f_s \pm f_m; f_s = 10 \text{ kHz}$

$$f_m = rac{8\pi imes 10^3}{2\pi} = 4
m kHz$$

Hence 14 kHz sinusoidal signal will be present

- **MCQ 1.62** Consider the sequence x[n] = [-4 j51 + j25]. The conjugate anti-symmetric part of the sequence is
 - (A) [-4-j2.5, j2, 4-j2.5] (B) [-j2.5, 1, j2.5](C) [-j2.5, j2, 0] (D) [-4, 1, 4]

SOL 1.62 Hence (A) is correct answer. We have x(n) = [-4 - j5, 1 + 2j, 4]

$$x^{*}(n) = \begin{bmatrix} -4 + j5, & 1 - 2j, & 4 \end{bmatrix}$$

$$x^{*}(-n) = \begin{bmatrix} 4, & 1 - 2j, & -4 + j5 \end{bmatrix}$$

$$x_{cas}(n) = \frac{x(n) - x^{*}(-n)}{2}$$

$$= \begin{bmatrix} -4 - j\frac{5}{2}, & 2j & 4 - j\frac{5}{2} \end{bmatrix}$$

MCQ 1.63 A causal LTI system is described by the difference equation $2y[n] = \alpha y[n-2] - 2x[n] + \beta x[n-1]$

The system is stable only if

$(\mathbf{A}) \alpha = 2, \beta < 2$	$(B) \alpha > 2, \beta > 2$
(C) $ \alpha < 2$, any value of β	(D) $ \beta < 2$, any value of α

SOL 1.63 Hence (C) is correct answer. We have $2y(n) = \alpha y(n-2) - 2x(n) + \beta x(n-1)$ Taking z transform we get $2Y(z) = \alpha Y(z) z^{-2} - 2X(z) + \beta X(z) z^{-1}$

or

or

$$\frac{Y(z)}{X(z)} = \left(\frac{\beta z^{-1} - 2}{2 - \alpha z^{-2}}\right)$$
$$H(z) = \frac{z(\frac{\beta}{2} - z)}{(z^2 - \frac{\alpha}{2})}$$

It has poles at $\pm \sqrt{\alpha/2}$ and zero at 0 and $\beta/2$. For a stable system poles must lie inside the unit circle of z plane. Thus

$$\left|\sqrt{\frac{\alpha}{2}}\right| < 1$$

$$|\alpha| < 2$$

But zero can lie anywhere in plane. Thus, β can be of any value.

...(i)

Page 29

A causal system having the transfer function H(s) = 1/(s+2) is excited with **MCQ 1.64** 10u(t). The time at which the output reaches 99% of its steady state value is (A) $2.7 \, \text{sec}$ (B) 2.5 sec(C) 2.3 sec(D) 2.1 sec **SOL 1.64** Hence (C) is correct option. We have r(t) = 10u(t) $R(s) = \frac{10}{s}$ or Now $H(s) = \frac{1}{s+2}$ $C(s) = H(s) \cdot R(s) = \frac{1}{s+2} \cdot \frac{10}{s} \frac{10}{s(s+2)}$ $C(s) = \frac{5}{s} - \frac{5}{s+2}$ or $c(t) = 5[1 - e^{-2t}]$ The steady state value of c(t) is 5. It will reach 99% of steady state value reaches at t, where $5[1 - e^{-2t}] = 0.99 \times 5$ or $1 - e^{-2t} = 0.99$ $e^{-2t} = 0.1$ **g at e** $t = 2.3 \operatorname{sec}$ or The impulse response h[n] of a linear time invariant system is given as **MCQ 1.65** $h[n] = \begin{cases} -2\sqrt{2} & n = 1, -1 \\ 4\sqrt{2} & n = 2, -2 \\ 0 & \text{otherwise} \end{cases}$ If the input to the above system is the sequence $e^{j\pi n/4}$, then the output is (A) $4\sqrt{2} e^{j\pi n/4}$ (B) $4\sqrt{2} e^{-j\pi n/4}$

(C)
$$4e^{j\pi n/4}$$
 (D) $-4e^{j\pi n/4}$

SOL 1.65 Hence (D) is correct answer. We have $x(n) = e^{j\pi n/4}$

 $\begin{aligned} x(n) &= e^{j\pi n/4} \\ h(n) &= 4\sqrt{2}\,\delta(n+2) - 2\sqrt{2}\,\delta(n+1) - 2\sqrt{2}\,\delta(n-1) \end{aligned}$

 $+4\sqrt{2}\delta(n-2)$

Now

and

$$y(n) = x(n)^* h(n)$$

= $\sum_{k=-\infty}^{\infty} x(n-k)h(k) = \sum_{k=-2}^{2} x(n-k)h(k)$
 $y(n) = x(n+2)h(-2) + x(n+1)h(-1)$
 $+ x(n-1)h(1) + x(n-2)h(2)$
= $4\sqrt{2} e^{j\frac{\pi}{4}(n+2)} - 2\sqrt{2} e^{j\frac{\pi}{4}(n+1)} - 2\sqrt{2} e^{j\frac{\pi}{4}(n-1)} + 4\sqrt{2} e^{j\frac{\pi}{4}(n-2)}$

or

or

$$\begin{split} &= 4\sqrt{2} \big[e^{j\frac{\pi}{4}(n+2)} + e^{j\frac{\pi}{4}(n-2)} \big] - 2\sqrt{2} \big[e^{j\frac{\pi}{4}(n+1)} + e^{j\frac{\pi}{4}(n-1)} \big] \\ &= 4\sqrt{2} \, e^{j\frac{\pi}{4}n} \big[e^{j\frac{\pi}{2}} + e^{-j\frac{\pi}{2}} \big] - 2\sqrt{2} \, e^{j\frac{\pi}{2}n} \big[e^{j\frac{\pi}{4}} + e^{-j\frac{\pi}{4}} \big] \\ &= 4\sqrt{2} \, e^{j\frac{\pi}{4}n} \big[0 \big] - 2\sqrt{2} \, e^{j\frac{\pi}{4}n} \big[2\cos\frac{\pi}{4} \big] \\ y(n) = -4e^{j\frac{\pi}{4}n} \end{split}$$

MCQ 1.66 Let x(t) and y(t) with Fourier transforms F(f) and Y(f) respectively be related as shown in Fig. Then Y(f) is



(C)
$$90^{\circ}$$
 (D) -180°

SOL 1.67 Approximate (comparable to 90°) phase shift are Due to pole at $0.01 \text{ Hz} \rightarrow -90^{\circ}$ Due to pole at $80 \text{ Hz} \rightarrow -90^{\circ}$ Due to pole at $80 \text{ Hz} \rightarrow 0$ Due to zero at $5 \text{ Hz} \rightarrow 90^{\circ}$ Due to zero at 100 Hz $\rightarrow 0$ Due to zero at 200 Hz $\rightarrow 0$ Thus approximate total -90° phase shift is provided. Hence (A) is correct option.

Consider the signal flow graph shown in Fig. The gain $\frac{x_5}{x}$ is **MCQ 1.68**

 $\Delta = 1 - (\text{sum of individual loops}) - (\text{Sum of two non touching loops})$ $= 1 - (L_1 + L_2 + L_3) + (L_1 L_3)$ Non touching loop are L_1 and L_3 where

 $L_1L_2 = bedg$

Thus
$$\frac{C(s)}{R(s)} = \frac{p_1 \Delta_1}{1 - (be + cf + dg) + bedg}$$
$$= \frac{abcd}{1 - (be + cf + dg) + bedg}$$

Hence (C) is correct option

$$\begin{array}{ll} \textbf{MCQ 1.69} & \text{If } A = \begin{bmatrix} -2 & 2 \\ 1 & -3 \end{bmatrix}, \text{ then } \sin At \text{ is} \\ & (A) \; \frac{1}{3} \begin{bmatrix} \sin \left(-4t \right) + 2\sin \left(-t \right) \; -2\sin \left(-4t \right) + 2\sin \left(-t \right) \\ -\sin \left(-4t \right) + \sin \left(-t \right) \; \; 2\sin \left(-4t \right) + \sin \left(-t \right) \end{bmatrix} \\ & (B) \; \begin{bmatrix} \sin \left(-2t \right) \; \sin \left(2t \right) \\ \sin \left(t \right) \; \sin \left(-3t \right) \end{bmatrix} \\ & (C) \; \frac{1}{3} \begin{bmatrix} \sin \left(4t \right) + 2\sin \left(t \right) \; \; 2\sin \left(-4t \right) - 2\sin \left(-t \right) \\ -\sin \left(-4t \right) + \sin \left(t \right) \; \; 2\sin \left(4t \right) + \sin \left(t \right) \end{bmatrix} \\ & (D) \; \frac{1}{3} \begin{bmatrix} \cos \left(-t \right) + 2\cos \left(t \right) \; \; 2\cos \left(-4t \right) + 2\cos \left(-t \right) \\ -\cos \left(-4t \right) + \cos \left(-t \right) \; \; -2\cos \left(-4t \right) + \cos \left(t \right) \end{bmatrix} \end{array}$$

Brought to you by: Nodia and Company PUBLISHING FOR GATE

Visit us at: www.nodia.co.in

SOL 1.68

SOL 1.69 Hence (A) is correct option
We have
$$A = \begin{bmatrix} -2 & 2 \\ 1 & -3 \end{bmatrix}$$

Characteristic equation is
 $[\lambda I - A] = 0$
or $\begin{vmatrix} \lambda + 2 & -2 \\ -1 & \lambda + 3 \end{vmatrix} = 0$
or $(\lambda + 2)(\lambda + 3) - 2 = 0$
or $\lambda^2 + 5\lambda + 4 = 0$
Thus $\lambda_1 = -4$ and $\lambda_2 = -1$
Eigen values are -4 and -1 .
Eigen values are -4 and -1 .
Eigen vectors for $\lambda_1 = -4$
 $(\lambda_1 I - A) X_1 = 0$
or $\begin{bmatrix} \lambda_1 + 2 & -2 \\ 1 & \lambda_1 + 3 \end{bmatrix} \begin{bmatrix} x_{11} \\ x_{21} \end{bmatrix} = 0$
or $\begin{bmatrix} -2 & -2 \\ -1 & -1 \end{bmatrix} \begin{bmatrix} x_{11} \\ x_{21} \end{bmatrix} = 0$
or $-2x_{11} - 2x_{21} = 0$
We have only one independent equation $x_{11} = -x_{21}$.
Let $x_{21} = K$, then $x_{11} = -K$, the Eigen vector will be

$$\begin{bmatrix} x_{11} \\ x_{21} \end{bmatrix} = \begin{bmatrix} -K \\ K \end{bmatrix} = K \begin{bmatrix} -1 \\ 1 \end{bmatrix}$$

Now Eigen vector for $\lambda_2 = -1$ $(\lambda_2 I - A) X_2 = 0$

or
$$\begin{bmatrix} \lambda_2 + 2 & -2 \\ -1 & \lambda_2 + 3 \end{bmatrix} \begin{bmatrix} x_{12} \\ x_{22} \end{bmatrix} = 0$$

or
$$\begin{bmatrix} 1 & -2 \\ -1 & 2 \end{bmatrix} \begin{bmatrix} x_{12} \\ x_{22} \end{bmatrix} = 0$$

We have only one independent equation $x_{12} = 2x_{22}$ Let $x_{22} = K$, then $x_{12} = 2K$. Thus Eigen vector will be

$$\begin{bmatrix} x_{12} \\ x_{22} \end{bmatrix} = \begin{bmatrix} 2K \\ K \end{bmatrix} = K \begin{bmatrix} 2 \\ 1 \end{bmatrix}$$

Digonalizing matrix

$$M = \begin{bmatrix} x_{11} & x_{12} \\ x_{21} & x_{22} \end{bmatrix} = \begin{bmatrix} -1 & 2 \\ 1 & 1 \end{bmatrix}$$

Now
$$M^{-1} = \left(\frac{-1}{3}\right) \begin{bmatrix} 1 & -2 \\ -1 & -1 \end{bmatrix}$$

Brought to you by: Nodia and Company PUBLISHING FOR GATE Visit us at: www.nodia.co.in

GATE EC 2004

Now Diagonal matrix of $\sin At$ is D where $D = \begin{bmatrix} \sin(\lambda_1 t) & 0 \\ 0 & \sin(\lambda_2 t) \end{bmatrix} = \begin{bmatrix} \sin(-4t) & 0 \\ 0 & \sin(\lambda_2 t) \end{bmatrix}$ Now matrix $B = \sin At = MDM^{-1}$

$$= -\left(\frac{1}{3}\right) \begin{bmatrix} -1 & 2\\ 1 & 1 \end{bmatrix} \begin{bmatrix} \sin\left(-4t\right) & 0\\ 0 & \sin\left(-t\right) \end{bmatrix} \begin{bmatrix} 1 & -2\\ -1 & -1 \end{bmatrix}$$
$$= -\left(\frac{1}{3}\right) \begin{bmatrix} -\sin\left(-4t\right) - 2\sin\left(-t\right) & 2\sin\left(-4t\right) - 2\sin\left(-t\right)\\ \sin\left(-4t\right) + 2\sin\left(t\right) & -2\sin\left(-4t\right) - 2\sin\left(-t\right) \end{bmatrix}$$
$$= -\left(\frac{1}{3}\right) \begin{bmatrix} -\sin\left(-4t\right) - 2\sin\left(-t\right) & 2\sin\left(-4t\right) - 2\sin\left(-t\right)\\ \sin\left(-4t\right) - \sin\left(-t\right) & -2\sin\left(-4t\right) + 2\sin\left(-t\right) \end{bmatrix}$$
$$= \left(\frac{1}{3}\right) \begin{bmatrix} \sin\left(-4t\right) + 2\sin\left(-t\right) & -2\sin\left(-4t\right) + 2\sin\left(-t\right)\\ -\sin\left(-4t\right) + 2\sin\left(-t\right) & 2\sin\left(-4t\right) + 2\sin\left(-t\right) \end{bmatrix} s$$

MCQ 1.70 The open-loop transfer function of a unity feedback system is

$$G(s) = \frac{K}{s(s^2 + s + 2)(s + 3)}$$

The range of K for which the system is stable is

(A)
$$\frac{21}{4} > K > 0$$
Y a i G (B) $13 > K > 0$

 (C) $\frac{21}{4} < K < \infty$
Figure 10 i G (D) $-6 < K < \infty$

SOL 1.70

1.70 For ufb system the characteristic equation is
$$1 + G(s) = 0$$

$$1 + \frac{K^{1+G(s)}}{s(s^2 + 2s + 2)(s+3)} = 0$$

$$s^4 + 4s^3 + 5s^2 + 6s + K = 0$$

The routh table is shown below. For system to be stable,

$$0 < K$$
 and $0 < \frac{(21 - 4K)}{2/7}$

This gives 0 $< K < \frac{21}{4}$

s^4	1	5	K	
s^3	4	6	0	
s^2	$\frac{7}{2}$	K		
s^1	$\frac{21-4K}{7/2}$	0		
s^0	K			

Hence (A) is correct option

For the polynomial $P(s) = s^2 + s^4 + 2s^3 + 2s^2 + 3s + 15$ the number of roots which **MCQ 1.71** lie in the right half of the s-plane is

- (A) 4(B) 2
- (C) 3 (D) 1

Hence (B) is correct option. SOL 1.71 We have $P(s) = s^5 + s^4 + 2s^3 + 3s + 15$ The routh table is shown below.

If $\varepsilon \to 0^+$ then $\frac{2\varepsilon+12}{\varepsilon}$ is positive and $\frac{-15\varepsilon^2-24\varepsilon-144}{2\varepsilon+12}$ is negative. Thus there are two sign change in first column. Hence system has 2 root on RHS of plane.

s^5	1	2	3	
s^4	1	2	15	
s^3	ε	-12	0	
s^2	$\frac{2\varepsilon + 12}{\varepsilon}$	15	0	
s^1	$\frac{-15\varepsilon^2 - 24\varepsilon - 144}{2\varepsilon + 12}$			
s^0	0			
y a l c				

The state variable equations of a system are : $\dot{x_1} = -3x_1 - x_2 = u, \dot{x_2} = 2x_1$ and **MCQ 1.72** $y = x_1 + u$. The system is

- (A) controllable but not observable
- (B) observable but not controllable
- (C) neither controllable nor observable
- (D) controllable and observable

SOL 1.72

Hence (D) is correct option. We have $\begin{bmatrix} x_1 \\ x_2 \end{bmatrix} = \begin{bmatrix} -3 & -1 \\ 2 & 0 \end{bmatrix} \begin{bmatrix} x_1 \\ x_2 \end{bmatrix} + \begin{bmatrix} 1 \\ 0 \end{bmatrix} u$ $Y = \begin{bmatrix} 1 & 0 \end{bmatrix} \begin{bmatrix} x_1 \\ x_2 \end{bmatrix} + \begin{bmatrix} 1 \\ 2 \end{bmatrix} u$ and $A = \begin{bmatrix} -3 & -1 \\ 2 & 0 \end{bmatrix}, B = \begin{bmatrix} 1 \\ 0 \end{bmatrix} \text{ and } C = \begin{bmatrix} 1 & 0 \end{bmatrix}$ Here The controllability matrix is $Q_C = [B \ AB]$

$$= \begin{bmatrix} 1 & -3 \\ 0 & 2 \end{bmatrix}$$

det $Q_C \neq 0$ The observability matrix is Thus controllable

Visit us at: www.nodia.co.in

www.gatehelp.com



MCQ 1.74 Consider the signal x(t) shown in Fig. Let h(t) denote the impulse response of the filter matched to x(t), with h(t) being non-zero only in the interval 0 to 4 sec. The slope of h(t) in the interval 3 < t < 4 sec is



$$h(t) = x(4-t)$$

The graph of h(t) is as shown below.

SOL 1.74



From graph it may be easily seen that slope between 3 < t < 4 is -1. Hence (B) is correct option.

MCQ 1.75 A 1 mW video signal having a bandwidth of 100 MHz is transmitted to a receiver through cable that has 40 dB loss. If the effective one-side noise spectral density at the receiver is 10⁻²⁰ Watt/Hz, then the signal-to-noise ratio at the receiver is

(B) 30 dB

(C) 40 dB
(D) 60 dB
SOL 1.75 The SNR at transmitter is

$$SNR_{tr} = \frac{P_{tr}}{\mathbb{N}B}$$

 $\frac{10^{-3}}{10^{-20} \times 100 \times 10^6} = 10^9$
In dB $SNR_{tr} = 10 \log 10^9$
Cable Loss = 40 db
At receiver after cable loss we have
 $SNR_{Rc} = 90 - 40 = 50$ dB
Hence (A) is correct option.

(A) 50 dB

MCQ 1.76 A 100 MHz carrier of 1 V amplitude and a 1 MHz modulating signal of 1 V amplitude are fed to a balanced modulator. The ourput of the modulator is passed through an ideal high-pass filter with cut-off frequency of 100 MHz. The output of the filter is added with 100 MHz signal of 1 V amplitude and 90° phase shift as shown in the figure. The envelope of the resultant signal is



The output of balanced modulator is

$$V_{BM}(t) = [\cos \omega_c t] [\cos \omega_c t]$$

= $\frac{1}{2} [\cos (\omega_c + \omega_m) t + \cos (\omega_c - \omega_m) t]$

 $V_0(t) = V_{HP}(t) + \sin(2\pi \times 100 \times 10^6) t$

If $V_{BM}(t)$ is passed through HPF of cut off frequency $f_H = 100 \times 10^6$, then only $(\omega_c + \omega_m)$ passes and output of HPF is

$$V_{HP}(t) = rac{1}{2} \cos\left(\omega_c + \omega_m
ight) t$$

Now

$$= \frac{1}{2} \cos \left[2\pi 100 \times 10^6 + 2\pi \times 1 \times 10^6 t \right] + \sin \left(2\pi \times 100 \times 10^6 \right) t$$

$$= \frac{1}{2} \cos \left[2\pi 10^8 + 2\pi 10^6 t \right] + \sin \left(2\pi 10^8 \right) t$$

$$= \frac{1}{2} \left[\cos \left(2\pi 10^8 t \right) t \cos \left(2\pi 10^6 t \right) \right] - \sin \left[2\pi 10^8 t \sin \left(2\pi 10^6 t \right) + \sin 2\pi 10^8 t \right]$$

$$= \frac{1}{2} \cos \left(2\pi 10^6 t \right) \cos 2\pi 10^8 t + \left(1 - \frac{1}{2} \sin 2\pi 10^6 t \right) \sin 2\pi 10^8 t$$

in form

This signal is in form

The envelope of this signal is

$$= A \cos 2\pi 10^8 t + B \sin 2\pi 10^8 t$$
The envelope of this signal is

$$= \sqrt{A^2 + B^2}$$

$$= \sqrt{\left(\frac{1}{2}\cos\left(2\pi 10^6 t\right)\right)^2 + \left(1 - \frac{1}{2}\sin\left(2\pi 10^6 t\right)\right)^2}$$

$$= \sqrt{\frac{1}{4}\cos^2(2\pi 10^6 t) + 1 + \frac{1}{4}\sin^2(2\pi 10^6 t) - \sin\left(2\pi 10^6 t\right)}$$

$$= \sqrt{\frac{1}{4} + 1 - \sin\left(2\pi 10^6 t\right)}$$

$$= \sqrt{\frac{5}{4} - \sin\left(2\pi 10^6 t\right)}$$

MCQ 1.77 Two sinusoidal signals of same amplitude and frequencies 10 kHz and 10.1 kHz are added together. The combined signal is given to an ideal frequency detector. The output of the detector is

SOL 1.77 Hence (A) is correct option.

$$s(t) = A\cos[2\pi 10 \times 10^{3} t] + A\cos[2\pi 10.1 \times 10^{3} t]$$
$$T_{1} = \frac{1}{10 \times 10^{3}} = 100\mu \sec$$

Here

and

$$T_2 = \frac{1}{10.1 \times 10^3} = 99\mu \sec \theta$$

Period of added signal will be LCM $[T_1, T_2]$

Brought to you by: <u>Nodia and Company</u> PUBLISHING FOR GATE

Thus
$$T = LCM[100,99] = 9900\mu \sec$$

Thus frequency $f = \frac{1}{9900\mu} = 0.1 \text{ kHz}$

MCQ 1.78 Consider a binary digital communication system with equally likely 0's and 1's. When binary 0 is transmitted the detector input can lie between the levels -0.25 V and +0.25 V with equal probability : when binary 1 is transmitted, the voltage at the detector can have any value between 0 and 1 V with equal probability. If the detector has a threshold of 0.2 V (i.e., if the received signal is greater than 0.2 V, the bit is taken as 1), the average bit error probability is (A) 0.15 (B) 0.2

$$(C) 0.05 (D) 0.5 (D) 0.5$$

SOL 1.78

1.78 The pdf of transmission of 0 and 1 will be as shown below :



. . . .

Hence (A) is correct option.

- **MCQ 1.79** A random variable X with uniform density in the interval 0 to 1 is quantized as follows :
 - If $0 \le X \le 0.3$, $x_q = 0$ If $0.3 < X \le 1$, $x_q = 0.7$ where x_q is the quantized value of X.The root-mean square value of the quantization noise is(A) 0.573(B) 0.198(C) 2.205(D) 0.266
- **SOL 1.79** Hence (B) is correct option. The square mean value is

$$\sigma^2 = \int_{-\infty}^{\infty} (x - x_q)^2 f(x) \, dx$$

MCQ

SOL

Q

$$= \int_{0}^{1} (x - x_{0})^{2} f(x) dx$$

$$= \int_{0}^{0.3} (x - 0)^{2} f(x) dx + \int_{0.3}^{0.1} (x - 0.7)^{2} f(x) dx$$

$$= \left[\frac{x^{3}}{3}\right]_{0}^{0.3} + \left[\frac{x^{3}}{3} + 0.49x - 14\frac{x^{2}}{2}\right]_{0.3}^{1}$$
or
$$\sigma^{2} = 0.039$$
RMS = $\sqrt{\sigma^{2}} = \sqrt{0.039} = 0.198$
1.80 Choose the current one from among the alternative *A*, *B*, *C*, *D* after matching an item from Group 1 with the most appropriate item in Group 2.
Group 1
Group 2
1. FM
P. Slope overload
2. DM
Q. μ -law
3. PSK
R. Envelope detector
4. PCM
S. Hilbert transform
T. Hilbert transform
U. Matched filter
(A) 1 - T, 2 - P, 3 - U, 4 - S
(C) 1 - S, 2 - P, 3 - U, 4 - S
(C) 1 - S, 2 - P, 3 - U, 4 - S
(C) 1 - S, 2 - P, 3 - U, 4 - S
(C) 1 - S, 2 - P, 3 - U, 4 - S
(C) 1 - S, 2 - P, 3 - U, 4 - S
(C) 1 - S, 2 - P, 3 - U, 4 - S
(C) 1 - S, 2 - P, 3 - U, 4 - S
(C) 1 - S, 2 - P, 3 - U, 4 - S
(C) 1 - S, 2 - P, 3 - U, 4 - S
(C) 1 - S, 2 - P, 3 - U, 4 - S
(C) 1 - S, 2 - P, 3 - U, 4 - B
(D) 1 - U, 2 - R, 3 - S, 4 - Q

1.80 Hence (C) is correct option.
FM \rightarrow Capture effect
DM \rightarrow Slope over load
PSK \rightarrow Matched filter
PCM $\rightarrow \mu$ - law

MCQ 1.81 Three analog signals, having bandwidths 1200 Hz, 600 Hz and 600 Hz, are sampled at their respective Nyquist rates, encoded with 12 bit words, and time division multiplexed. The bit rate for the multiplexed. The bit rate for the multiplexed signal is (\mathbf{D}) $\mathbf{a} \mathbf{a} \mathbf{a} \mathbf{b}$ $\mathbf{1}$

(A) 115.2 kbps	(B) 28.8 kbps
(C) 57.6 kbps	(D) 38.4 kbps

SOL 1.81 Since $f_s = 2f_m$, the signal frequency and sampling frequency are as follows

 $f_{m1} = 1200 \text{ Hz} \longrightarrow 2400 \text{ samples per sec}$

 $f_{m2} = 600 \text{ Hz} \longrightarrow 1200 \text{ samples per sec}$

 $f_{m3} = 600 \text{ Hz} \longrightarrow 1200 \text{ samples per sec}$

Thus by time division multiplexing total 4800 samples per second will be sent. Since each sample require 12 bit, total 4800×12 bits per second will be sent Thus bit rate $R_b = 4800 \times 12 = 57.6$ kbps Hence (C) is correct option.

Consider a system shown in the figure. Let X(f) and Y(f) and denote the Fourier **MCQ 1.82**

Brought to you by: Nodia and Compa
PUBLISHING FO

Page 40

transforms of x(t) and y(t) respectively. The ideal HPF has the cutoff frequency 10 kHz.



GATE EC 2004

MCQ 1.84 A source produces binary data at the rate of 10 kbps. The binary symbols are represented as shown in the figure given below.



The source output is transmitted using two modulation schemes, namely Binary PSK (BPSK) and Quadrature PSK (QPSK). Let B_1 and B_2 be the bandwidth requirements of BPSK and QPSK respectively. Assume that the bandwidth of he above rectangular pulses is 10 kHz, B_1 and B_2 are

(A)
$$B_1 = 20 \text{ kHz}, B_2 = 20 \text{ kHz}$$

(B) $B_1 = 10 \text{ kHz}, B_2 = 20 \text{ kHz}$
(C) $B_1 = 20 \text{ kHz}, B_2 = 10 \text{ kHz}$
(D) $B_1 = 20 \text{ kHz}, B_2 = 10 \text{ kHz}$

SOL 1.84 The required bandwidth of *M* array PSK is

$$BW = \frac{2R_b}{n}$$

where $2^n = M$ and R_b is bit rate For BPSK, $M = 2 = 2^n \longrightarrow n = 1$

Thus

$$B_1 = \frac{2R_b}{1} = 2 \times 10 = 20 \text{ kHz}$$
$$M = 4 = 2^n \longrightarrow n = 2$$

For QPSK,

Thus

$$B_2 = \frac{2R_b}{2} = 10 \text{ kHz}$$

Hence (C) is correct option.

MCQ 1.85 Consider a 300 Ω , quarter - wave long (at 1 GHz) transmission line as shown in Fig. It is connected to a 10 V, 50 Ω source at one end and is left open circuited at the other end. The magnitude of the voltage at the open circuit end of the line is



1

or
$$V_L = \frac{Z_0}{Z_{in}} V_{in} = \frac{10 \times 300}{50} = 60 \text{ V}$$

MCQ 1.86 In a microwave test bench, why is the microwave signal amplitude modulated at 1 kHz
(A) To increase the sensitivity of measurement
(B) To transmit the signal to a far-off place
(C) To study amplitude modulations
(D) Because crystal detector fails at microwave frequencies
SOL 1.86 Hence (D) is correct option.
MCQ 1.87 If $\vec{E} = (\hat{a}_x + j\hat{a}_y) e^{jkz \cdot k\omega t}$ and $\vec{H} = (k/\omega\mu) (\hat{a}_y + k\hat{a}_x) e^{jkz \cdot j\omega t}$, the time-averaged Poynting
vector is
(A) null vector
(B) $(k/\omega\mu) \hat{a}_z$
SOL 1.87 Hence (A) is correct option.
 $R_{avy} = \frac{1}{2} \operatorname{Re}[\vec{E} \times \vec{H}]$
 $\vec{E} \times \vec{H} = (\hat{a}_x + j\hat{a}_y) e^{jkz \cdot \vec{E} \cdot \vec{L}} \frac{k}{\omega\mu} (-j\hat{a}_x + \hat{a}_y) e^{-jkz + j\omega t}$
 $= \hat{a}_z [\frac{k}{\omega\mu} - (-j) (j) \frac{k}{\omega\mu}]$
Thus $R_{avg} = \frac{1}{2} \operatorname{Re}[\vec{E} \times \vec{H}] = 0$

Consider an impedance Z = R + jX marked with point P in an impedance Smith **MCQ 1.88** chart as shown in Fig. The movement from point P along a constant resistance circle in the clockwise direction by an angle 45° is equivalent to



(A) adding an inductance in series with Z

- (B) adding a capacitance in series with Z
- (C) adding an inductance in shunt across Z
- (D) adding a capacitance in shunt across Z

Page 43

Suppose at point P impedance is **SOL 1.88**

$$Z = r + j(-1)$$

5i

If we move in constant resistance circle from point P in clockwise direction by an angle 45°, the reactance magnitude increase. Let us consider a point Q at 45° from point P in clockwise direction. It's impedance is

$$Z_1 = r - 0.$$

$$Z_1 = Z + 0.5j$$

Thus movement on constant r - circle by an $\angle 45^{\circ}$ in CW direction is the addition of inductance in series with Z.

Hence (A) is correct option.

MCQ 1.89 A plane electromagnetic wave propagating in free space is incident normally on a large slab of loss-less, non-magnetic, dielectric material with $\varepsilon > \varepsilon_0$. Maxima and minima are observed when the electric field is measured in front of the slab. The maximum electric field is found to be 5 times the minimum field. The intrinsic impedance of the medium should be

	(A) $120\pi \Omega$ (C) $600\pi \Omega$	(B) $60\pi \Omega$ (D) $24\pi \Omega$
SOL 1.89	Hence (D) is	correct option.
	We have	$VSWR = \frac{E_{max}}{E_{min}} = 5 = \frac{1 - \Gamma}{1 + \Gamma }$
	or	$ \Gamma = \frac{2}{3}$ Nelp
	Thus	$\Gamma = -\frac{2}{3}$
	Now	$\Gamma = rac{\eta_2 - \eta_1}{\eta_2 + \eta_1}$
	or	$-rac{2}{3}=rac{\eta_2-120\pi}{\eta_2+120\pi}$
	or	$\eta_2 = 24\pi$
MCQ 1.90	A lossless ti	cansmission line is terminated in a load

ad which reflects a part of the Μ incident power. The measured VSWR is 2. The percentage of the power that is reflected back is

(A) 57.73	(B) 33.33
(C) 0.11	(D) 11.11

SOL 1.90 Hence (D) is correct option. $2 = \frac{1 - |\Gamma|}{1 + |\Gamma|}$ The VSWR $|\Gamma| = \frac{1}{3}$

Thus

$$\frac{P_{ref}}{P_{inc}} = |\Gamma|^2 = \frac{1}{9}$$
$$P_{ref} = \frac{P_{inc}}{9}$$

or

i.e. 11.11% of incident power is reflected.

Answer Sheet									
1.	(B)	19.	(A)	37.	(D)	55.	(B)	73.	(B)
2.	(D)	20.	(D)	38.	(B)	56.	(C)	74.	(B)
3.	(A)	21.	(D)	39.	(B)	57.	(A)	75.	(A)
4.	(A)	22.	(C)	40.	(D)	58.	(D)	76.	(C)
5.	(C)	23.	(D)	41.	(C)	59.	(D)	77.	(A)
6.	(C)	24.	(C)	42.	(D)	60.	(C)	78.	(A)
7.	(A)	25.	(C)	43.	(A)	61.	(D)	79.	(B)
8.	(D)	26.	(B)	44.	(B)	62.	(A)	80.	(C)
9.	(B)	27.	(B)	45.	(C)	63.	(C)	81.	(C)
10.	(C)	28.	(A)	46.	(D)	64.	(C)	82.	(B)
11.	(A)	29.	(D)	47.	(A)	65.	(D)	83.	(D)
12.	(A)	30.	(A)	48.	(A)	66.	(B)	84.	(C)
13.	(C)	31.	(D)	49.	(D)	67.	(A)	85.	(C)
14.	(A)	32.	(B)	50.	(B)	68.	(C)	86.	(D)
15.	(D)	33.	(C)	51.	(C)	69.	(A)	87.	(A)
16.	(B)	34.	(B)	52.	(D)	70	(A)	88.	(A)
17.	(A)	35.	(D)	53.	(C)	71	(B)	89.	(D)
18.	(C)	36.	(A)	54.	(D)	72	(D)	90.	(D)

Exclusive Series By Jhunjhunuwala

GATE CLOUD

By R. K . Kanodia & Ashish Murolia

GATE Cloud is an exclusive series of books which offers a completely solved question bank to GATE aspirants. The book of this series are featured as

- > Over 1300 Multiple Choice Questions with full & detailed explanations.
- > Questions are graded in the order of complexity from basic to advanced level.
- Contains all previous year GATE and IES exam questions from various branches
- > Each question is designed to GATE exam level.
- > Step by step methodology to solve problems

Available Title In this series

- **Signals and Systems (For EC and EE)**
- Network Analysis (For EC)-- Available in 2 Volumes
- Electric Circuit and Fields (For EE) -- Available in two volumes
- Electromagnetic (For EC)

Upcoming titles in this series

- Digital Electronics (Nov 2012)
- Control Systems (Dec 2012)
- Communication Systems (Jan 2012)

Exclusive Series By Jhunjhunuwala

GATE GUIDE

Theory, Example and Practice By R. K. Kanodia & Ashish Murolia

GATE GUIDE is an exclusive series of books which provides theory, solved examples & practice exercises for preparing for GATE. A book of this series includes :

- > Brief and explicit theory
- > Problem solving methodology
- > Detailed explanations of examples
- > Practice Exercises

Available Title In this series

- **Signals and Systems (For EC and EE)**
- Network Analysis (For EC)
- **Electric Circuit and Fields (For EE)**

Upcoming titles in this series

- Digital Electronics(For EC and EE)
- **Control Systems (For EC and EE)**
- Communication Systems (For EC and EE)