MODEL QUESTION PAPER FOR TERM 1 TEST

II PU Electronics [40]

Instructions

- a. The Question paper has five parts A, B, C, D and E
- b. Part A is compulsory
- c. Part D contains only problems
- d. Read the instruction given for each part

PART A

Answer ALL questions

- 1. In which region of characteristics a transistor behaves as closed switch?
- 2. What is faithful amplification?
- 3. What is buffer amplifier?
- 4. Write the symbol of XNOR gate.
- 5. Give one example for weighted code.

PART B

Answer any THREE questions

- 6. Write a note on the selection of Q point.
- 7. Write the steps involved in drawing DC equivalent circuit of an amplifier.
- 8. Define class B and class C power amplifiers.
- 9. Convert the SOP Boolean expression $Y = A\overline{B} + B\overline{C} + ABC$ into canonical form.
- 10. Write the logic circuit of D flip flop using only NAND gates.

PART C

Answer any THREE questions

- 11. Define the terms Thermal runaway, Heat sink and Leakage current.
- 12. Write any one application of CE, CB and CC amplifier.
- 13. Why do we express the gain in decibels?
- 14. Write the logic circuit and Boolean expressions for half subtractor.
- 15. What is race around condition? How is it eliminated?

PART D

Answer any ONE question

16. Find the input resistance and voltage gain of the CE transistor amplifier for the data given below:

$$\begin{split} R_1 &= 47 K \Omega, \, R_2 = 12 K \Omega, \, R_C = 3.3 K \Omega, \\ R_E &= 1 K \Omega, \, R_L = 10 K \Omega, \, V_{CC} = 18 V, \\ \beta &= 100, \, V_{BE} = 0.3 V \text{ and } r_e' = 52 m V/I_{E.} \\ V_{in} &= 10 \, m V. \end{split}$$



Max Marks –35

 $5 \times 1 = 5$

 $3 \ge 2 = 6$

 $3 \times 3 = 9$

 $1 \ge 5 = 5$

17. Simplify the following Boolean expression using K map $Y = \Sigma m(0,2,4,8,10) + \Sigma d(12, 14)$ Draw the logic circuit for the simplified expression using only NAND gates.

PART E

Answer any TWO questions

 $2 \ge 5 = 10$

18. Explain the working of CB amplifier

19. Explain the working of 2 stage RC coupled amplifier

20. Draw the pin diagram of IC7402. Realize the basic gates and XNOR gate using NOR gates.

21. Explain the working of SR Flip-Flop with logic circuit and truth table
