

ದಿನಾಂಕ : 23. 03. 2019] [Date : 23. 03. 2019 ಸಮಯ : ಬೆಳಿಗ್ಗೆ 9-30 ರಿಂದ ಮಧ್ಯಾಹ–12-45 ರವರೆಗೆ] [Time : 9-30 A.M. to 12-45 P.M. ಪರಮಾವಧಿ ಅಂಕಗಳು : 90] [Max. Marks : 90

General Instructions to the Candidate :

- This Question Paper consists of 9 objective and subjective types of questions.
- 2. This question paper has been sealed by reverse jacket. You have to cut on the right side to open the paper at the time of commencement of the examination. Check whether all the pages of the question paper are intact.
- Follow the instructions given against both the objective and subjective types of questions.
- 4. Figures in the right hand margin indicate maximum marks.
- 5. The maximum time to answer the paper is given at the top of the question paper. It includes 15 minutes for reading the question paper.

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<u>ဗျှစ်–ဗခ္</u>ပီနီလာလ

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			Note	: Answ	er <i>all</i> the questi	ons.						
1.	Fill :	ill in the blanks with the appropriate figure/word(s) by selecting from the										
	choi	oices given in the brackets : $10 \times 1 = 1$										
	i)	IC con	nsumes	po	ower.							
		(a)	more	(b)	less	(c)	none of these.					
	ii)	Norma	ally ICs are made	e of								
		(a)	Aluminium	(b)	Copper	(c)	Silicon.					
	iii)	ICs ar	e generally in th	e form	of	•						
		(a)	Flat pack	(b)	Triangle	(c)	Sphere.					
	iv)	IC 741 is an example of										
		(a)	MOSFET	(b)	Op-Amp	(c)	FET.					
	v)	Op-Ar	np has	impe	edance.							
		(a)	low	(b)	medium	(c)	high.					
	vi)	Octal	number system	has	logic	symb	ols.					
		(a)	8	(b)	4	(c)	6.					
	vii)	If the inputs of NAND gate IC are 1 & 1 then its output is										
		(a)	1	(b)	0	(c)	2.					
	viii)	Binar	ry number 0111	repres	ents	••••						
		(a)	9	(b)	8	(c)	7.					
	ix)	is used to store binary word temporary.										
		(a)	Buffer register	(b)	SISO register	(c)	SIPO register.					
	x)	is used to count number of clock pulses arrived at its										
		input.										
		(a)	Inverter	(b)	Counter	(c)	Converter.					
2.	a)	List any two active components.										
	b)	Explain monolithic IC.										
	c)	Draw a neat sketch to show the construction of monolithic IC. 5										
3.	a)	Define	e the term SSI.					2				
	b)	Describe how registors are fabricated in IC. 3										
	c)	Draw a neat sketch of CRT. 5										
4.	a)	List types of ICs based on manufacturing process.2Describe how hybrid ICs are differ from monolithic IC.3										
	b)											
	c)	Why ICs need less power to operate and more reliable than disc										
		components ?										
5.	a)	What is meant by an Op-Amp ?2										
	b)	Explain Inverting amplifier.										
	c)	Draw the block diagram of an Op-Amp and explain input stage.										

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6.	a)	Nam	Name the IC which inverts input data.				
	b)	Desc	ribe about universal logic gates.	3			
	c)	Dete	rmine the values of				
		i)	6 – 4 by using binary subtraction method				
		ii)	24 + 8 by using binary addition method.	5			
7.	a)	How	many digits are used in octal number system ?	2			
	b)	Why	binary number system is most popular ?	3			
	c)	Conv	vert octal numbers $(512)_8$ and $(1035)_8$ into decimal numbers.	5			
8.	a)	Defir	ne the term flip-flop.	2			
	b)	Expl	ain JK flip-flop.	3			
	c)	Draw	v a neat diagram of RS flip-flop using NAND gates and explain	. 5			
9.	a)	Wha	t do you mean by microprocessor ?	2			
	b)	Expl	ain 8085 microprocessor.	3			
	c)	Expl	ain Up and Down counter.	5			

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